

# < HVIGBT MODULES > CM1200HC-90RA

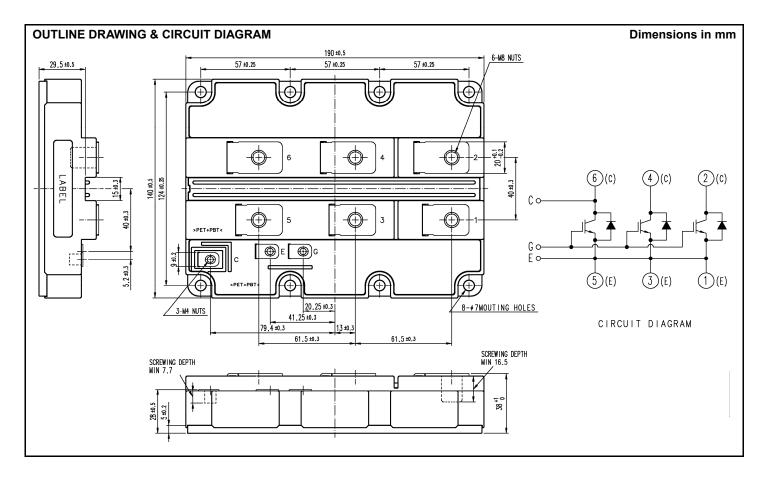
HIGH POWER SWITCHING USE INSULATED TYPE

4th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

# CM1200HC-90RA Ic. 1200A V<sub>CES</sub> 4500V 1-element in a pack High insulated type LPT-IGBT / Soft Recovery Diode AlSiC baseplate

### APPLICATION

HVDC, High Reliability Converters / Inverters, DC choppers



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## 4th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

#### MAXIMUM RATINGS

Symbol	Item	Conditions	Ratings	Unit
V	Collector emitter voltage	V <sub>GE</sub> = 0V, T <sub>j</sub> = +25+125°C	4500	v
V <sub>CES</sub>	Collector-emitter voltage	$V_{GE} = 0V, T_{j} = -40^{\circ}C$	3900	v
$V_{\text{GES}}$	Gate-emitter voltage	$V_{CE} = 0V, T_j = 25^{\circ}C$	± 20	V
I <sub>C</sub>	Collector current	DC, $T_c = 95^{\circ}C$	1200	А
I <sub>CRM</sub>	Collector current	Pulse (Note 1)	2400	А
I <sub>E</sub>	Emitter current (Note 2)	DC	1200	А
I <sub>ERM</sub>	Emitter current (Note 2)	Pulse (Note 1)	2400	А
P <sub>tot</sub>	Maximum power dissipation (Note 3)	$T_c$ = 25°C, IGBT part	13100	W
V <sub>iso</sub>	Isolation voltage	RMS, sinusoidal, f = 60Hz, t = 1 min.	6000	V
Ve	Partial discharge extinction voltage	RMS, sinusoidal, f = 60Hz, $Q_{PD} \le 10 \text{ pC}$	3500	V
T <sub>i</sub>	Junction temperature		-40 ~ +150	°C
T <sub>jop</sub>	Operating junction temperature		-40 ~ +125	°C
T <sub>stg</sub>	Storage temperature		-55 ~ +125	°C
t <sub>psc</sub>	Short circuit pulse width	V <sub>CC</sub> = 3000V, V <sub>CE</sub> ≤ V <sub>CES</sub> , V <sub>GE</sub> =15V, T <sub>j</sub> =125°C	10	μS

#### **ELECTRICAL CHARACTERISTICS**

Cumple al	DI Item Conditions				Limits	Unit	
Symbol	Item	Conditions	Conditions		Тур	Max	Unit
Ices Collector cutoff current	Calle star sutoff surrant		T <sub>j</sub> = 25°C	_		10.0	
ICES	Collector cutori current	$V_{CE} = V_{CES}, V_{GE} = 0V$	T <sub>j</sub> = 125°C	-	10.0	_	mA
$V_{GE(th)}$	Gate-emitter threshold voltage	V <sub>CE</sub> = 10 V, I <sub>C</sub> = 120 mA, T <sub>j</sub> = 25°C		5.8	6.3	6.8	V
I <sub>GES</sub>	Gate leakage current	$V_{GE}$ = $V_{GES}$ , $V_{CE}$ = 0V, $T_j$ = 25°C		-0.5	_	0.5	μA
Cies	Input capacitance	V <sub>CE</sub> = 10 V, V <sub>GE</sub> = 0 V, f = 100 kHz		_	180.0		nF
C <sub>oes</sub>	Output capacitance	$V_{CE} = 100$ V, $V_{GE} = 00$ V, $T = 100$ KHZ $T_i = 25^{\circ}C$		_	12.0		nF
C <sub>res</sub>	Reverse transfer capacitance	1j - 23 C			6.0	_	nF
$Q_{G}$	Total gate charge	$V_{CC}$ = 2800V, $I_{C}$ = 1200A, $V_{GE}$ = ±15V	V <sub>CC</sub> = 2800V, I <sub>C</sub> = 1200A, V <sub>GE</sub> = ±15V			_	μC
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> = 1200 A <sup>(Note 4)</sup>	T <sub>j</sub> = 25°C	_	2.80	_	v
V CEsat	Collector-entitier saturation voltage	V <sub>GE</sub> = 15 V	T <sub>j</sub> = 125°C	_	3.60	_	v
+	Turn-on delay time	V <sub>cc</sub> = 2800 V	T <sub>j</sub> = 25°C		_	_	us
t <sub>d(on)</sub>	rum-on delay time		T <sub>j</sub> = 125°C	_	0.95	1.50	μο
t,	Turn-on rise time	I <sub>C</sub> = 1200 A	T <sub>j</sub> = 25°C	_	_	_	
ι <sub>r</sub>	Turn-on rise time	$V_{GE} = \pm 15 V$	T <sub>j</sub> = 125°C		0.25	0.50	μs
E	Turn-on switching energy (Note 5)	R <sub>G(on)</sub> = 2.7 Ω	T <sub>j</sub> = 25°C	_			J
E <sub>on(10%)</sub>	rum-on switching energy	L <sub>s</sub> = 150 nH	T <sub>j</sub> = 125°C	_	5.40	_	5
Eon	Turn-on switching energy (Note 6)	Inductive load	T <sub>j</sub> = 25°C		_	_	J
Lon	rum-on switching energy		T <sub>j</sub> = 125°C	_	6.00		J
+	Turn-off delay time		T <sub>j</sub> = 25°C	_			
$t_{d(off)}$	rum-on delay time	V <sub>CC</sub> = 2800 V	T <sub>j</sub> = 125°C	_	8.10	10.0	μs
t,	Turn-off fall time	I <sub>C</sub> = 1200 A	T <sub>j</sub> = 25°C	_			us
ι <sub>f</sub>		$V_{GE} = \pm 15 V$	T <sub>j</sub> = 125°C	_	0.52	1.20	μο
E	Turn off quaitabing onergy (Note 5)	$R_{G(off)}$ = 27 $\Omega$	T <sub>j</sub> = 25°C	_	—	—	J
E <sub>off(10%)</sub>	Turn-off switching energy	L <sub>s</sub> = 150 nH	T <sub>j</sub> = 125°C		5.20		J
E <sub>off</sub>	Turn-off switching energy (Note 6)	ff switching energy (Note 6) Inductive load T <sub>j</sub>	T <sub>j</sub> = 25°C	_	—	—	J
∟off			T <sub>j</sub> = 125°C		5.60	_	5

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#### 4th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

Symbol	Item	0	onditions		Limits		Unit
Symbol	item			Min	Тур	Max	Unit
V <sub>FC</sub>	Emitter collector voltage (Note	I <sub>E</sub> = 1200 A <sup>(Note 4)</sup>	T <sub>j</sub> = 25°C	_	2.25		V
VEC	Emitter-collector voltage	$V_{GE} = 0 V$	T <sub>j</sub> = 125°C	_	2.50		v
+	Reverse recevent time (Note	2)	T <sub>j</sub> = 25°C	_	_		
trr	Reverse recovery time		T <sub>j</sub> = 125°C	_	0.90		μs
	Roverse receiver ourrent (Not	<sub>2)</sub> V <sub>CC</sub> = 2800 V	T <sub>j</sub> = 25°C	_	_		А
In	Reverse recovery current	I <sub>C</sub> = 1200 A	T <sub>j</sub> = 125°C	_	1400		A
0	Reverse recevent charge (Not	<sub>2)</sub> V <sub>GE</sub> = ±15 V	T <sub>j</sub> = 25°C	_	_		μC
Q <sub>rr</sub>	Reverse recovery charge	R <sub>G(on)</sub> = 2.7 Ω	T <sub>j</sub> = 125°C	_	1900		μΟ
<b>_</b>	Reverse recovery energy (Not	<sup>2)</sup> L <sub>s</sub> = 150 nH	T <sub>j</sub> = 25°C	_	_		
E <sub>rec(10%)</sub>	(Not	<sup>5)</sup> Inductive load	T <sub>j</sub> = 125°C	_	3.00		J
	Reverse recovery energy (Not	2)	T <sub>j</sub> = 25°C	_	_	_	
E <sub>rec</sub>	(Not	6)	T <sub>j</sub> = 125°C		3.30	_	J

#### ELECTRICAL CHARACTERISTICS (continuation)

#### THERMAL CHARACTERISTICS

Symbol	Item	Conditions		Limits		
Symbol	Item	Conditions	Min	Тур	Max	Unit
R <sub>th(j-c)Q</sub>	Thermal resistance	Junction to Case, IGBT part		_	9.5	K/kW
R <sub>th(j-c)D</sub>	Thermal resistance	Junction to Case, FWDi part		_	18.5	K/kW
R <sub>th(c-s)</sub>	Contact thermal resistance	Case to heat sink, $\lambda_{grease} = 1W/m \cdot k$ , $D_{(c-s)} = 100 \mu m$		6.0	_	K/kW

#### MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions	Limits	Unit		
	nem	Conditions	Min	Тур	Max	Unit
Mt		M8 : Main terminals screw	7.0	_	22.0	N∙m
Ms	Mounting torque	M6 : Mounting screw	3.0	_	6.0	N∙m
Mt		M4 : Auxiliary terminals screw	1.0	—	3.0	N∙m
m	Mass		—	1.2	_	kg
CTI	Comparative tracking index		600	_	_	_
d <sub>a</sub>	Clearance		19.5	_	_	mm
ds	Creepage distance		32.0	_	_	mm
L <sub>P CE</sub>	Parasitic stray inductance		—	11.0	_	nH
R <sub>CC'+EE'</sub>	Internal lead resistance	$T_{\rm C} = 25^{\circ}{\rm C}$	—	0.12	_	mΩ
r <sub>g</sub>	Internal gate resistance	$T_{\rm C} = 25^{\circ}{\rm C}$	_	1.7	_	Ω

Note1. Pulse width and repetition rate should be such that junction temperature (T<sub>j</sub>) does not exceed Topmax rating.

2. The symbols represent characteristics of the anti-parallel, emitter to collector free-wheel diode (FWD<sub>i</sub>).

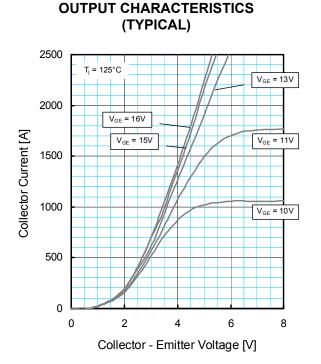
3. Junction temperature (T<sub>j</sub>) should not exceed  $T_{jmax}$  rating (150°C).

4. Pulse width and repetition rate should be such as to cause negligible temperature rise.

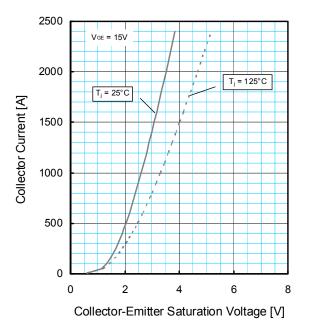
5.  $E_{on(10\%)} / E_{off(10\%)} / E_{rec(10\%)}$  are the integral of 0.1V<sub>CE</sub> x 0.1I<sub>C</sub> x dt.

6. Definition of all items is according to IEC 60747, unless otherwise specified.

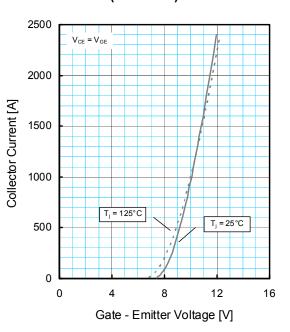
#### PERFORMANCE CURVES



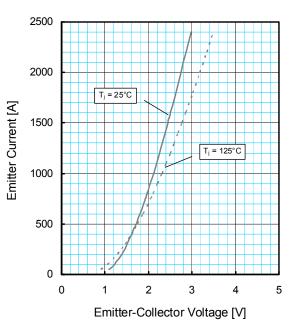
#### COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



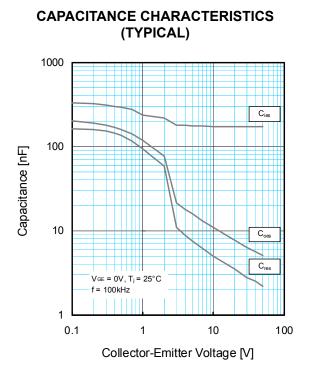
#### TRANSFER CHARACTERISTICS (TYPICAL)



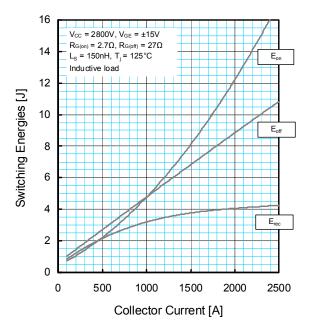
#### FREE-WHEEL DIODE FORWARD CHARACTERISTICS (TYPICAL)



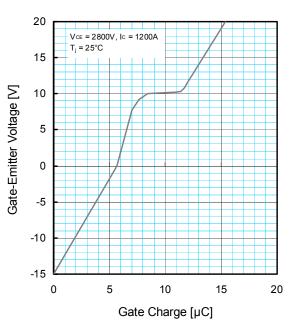
#### PERFORMANCE CURVES



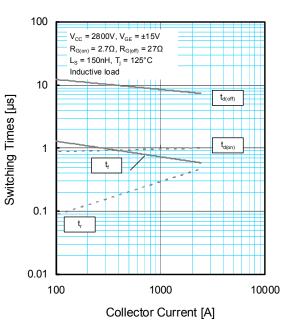
#### HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



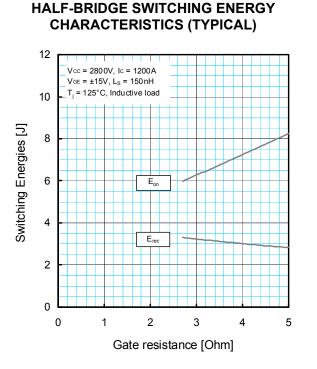
#### GATE CHARGE CHARACTERISTICS (TYPICAL)



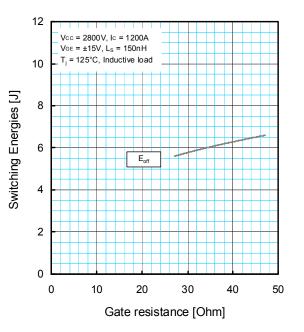
#### HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)



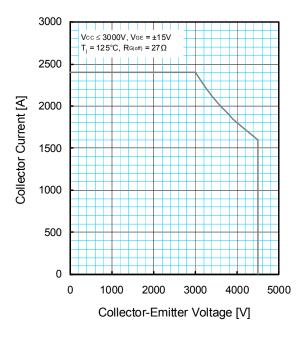
#### PERFORMANCE CURVES



#### HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)

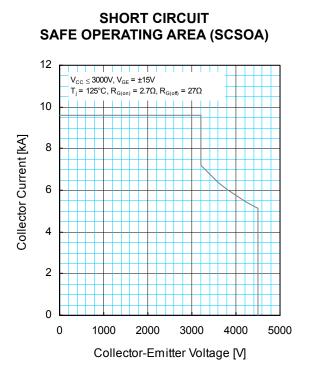


#### REVERSE BIAS SAFE OPERATING AREA (RBSOA)

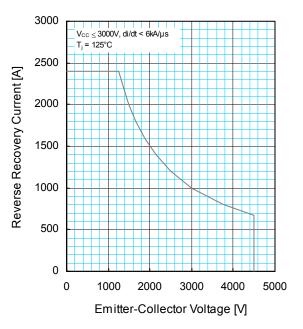


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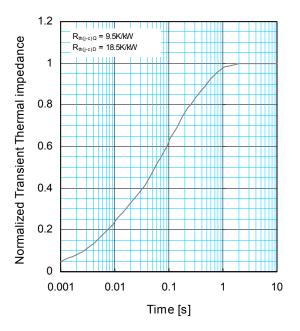
#### PERFORMANCE CURVES

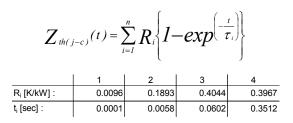


#### FREE-WHEEL DIODE REVERSE RECOVERY SAFE OPERATING AREA (RRSOA)



#### TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS





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